

## **IN THE CLAIMS:**

Please replace the text of claims 1-4 with the following text:

1. A solid-state image pickup apparatus comprising:
  - a solid-state image pickup device for generating an information charge in response to an image of which light was received;
  - a drive circuit for transferring the information charge accumulated in the solid-state image pickup device, and outputting the information charge;
  - a power supply for generating a predetermined voltage in accordance with an input amount of voltage booster pulses and supplying the predetermined voltage to the solid-state image pickup device and the drive circuit; and
  - a pulse generator circuit for generating and supplying the voltage booster pulses to the power supply;
  - wherein the pulse generator circuit stops generation of the voltage booster pulses in accordance with a termination of an information charge readout operation of one screen pickup period from the image pickup device, and when the drive circuit executes an electronic shutter operation, which discharges the information charge that has accumulated in the solid-state image pickup device to resume accumulation, the voltage booster pulses are generated over a predetermined voltage booster period prior to the electronic shutter operation at a higher frequency than the information charge readout operation to boost the voltage of the power supply.

2. The solid-state image pickup apparatus according to claim 1 wherein:

- a shutter timing is set for performing the electronic shutter operation at a subsequent screen pickup period in accordance with an exposure condition in an arbitrary image pickup period; and

a start timing is set for starting to performing the voltage boosting operation prior to the shutter timing by a predetermined time at least as long as the voltage booster period.

3. The solid-state image pickup apparatus according to claim 1 wherein:

a shutter trigger pulse having a predetermined pulse width at least as long as the voltage booster period is used;

the voltage boosting operation is initiated in connection with a timing of a leading edge of the shutter trigger pulse; and

the electronic shutter operation is initiated in connection with a timing of a trailing edge of the shutter trigger pulse.

4. The solid-state image pickup apparatus according to claim 1 wherein:

the stop operation of the voltage booster pulse circuit is prohibited when a start timing of the voltage boosting operation precedes the termination of the readout period of the information charge.

Please add the following new claims.

5. (Newly Added) The solid-state image pickup apparatus according to claim 1, wherein the one screen pickup period is one field in interlace scanning.

6. (Newly Added) The solid-state image pickup apparatus according to claim 1, wherein the one screen pickup period is one frame in non-interlace scanning.

7. (Newly Added) A method for supplying the power to a solid-state image pickup apparatus comprising:

- generating an information charge in response to an image of which light was received and accumulating the information charge in a solid-state device;
- transferring and outputting the information charge accumulated in the solid-state device;
- generating a predetermined voltage in accordance with an input amount of voltage booster pulses and supplying the predetermined voltage to the solid-state image pickup device and a drive circuit;
- generating and supplying the voltage booster pulses to a power supply;
- stop generating the voltage booster pulses in accordance with a termination of an information charge readout operation of one screen pickup period from the image pickup device; and
- executing an electronic shutter operation, which discharges the information charge that has accumulated in the solid-state image pickup device to resume the accumulation such that voltage booster pulses are generated over a predetermined voltage booster period prior to the electronic shutter operation at a higher frequency than the information charge readout operation to boost the voltage of the power supply.

8. (Newly Added) The method according to claim 7 further comprising:

- setting a shutter timing for performing the electronic shutter operation at a subsequent screen pickup period in accordance with an exposure condition in an arbitrary image pickup period; and
- setting a start timing for starting to perform the voltage boosting operation prior to the shutter timing by a predetermined time at least as long as the voltage booster period.

9. (Newly Added) The method according to claim 7 further comprising:

- using a shutter trigger pulse having a predetermined pulse width at least as long as the voltage booster period;
- initiating the voltage boosting operation in connection with a timing of a leading edge of the shutter trigger pulse; and
- initiating the electronic shutter operation in connection with a timing of a trailing edge of the shutter trigger pulse.

10. (Newly Added) The method according to claim 7 further comprising:

- prohibiting the stop operation of the voltage booster pulse when a start timing of a voltage boosting operation precedes the termination of the readout period of the information charge.

11. (Newly Added) The method according to claim 7 further comprising using one field in interlace scanning as the one screen pickup period.

12. (Newly Added) The method according to claim 7 further comprising using one frame in non-interlace scanning as the one screen pickup period.